

07/10/01

	Class	Subclass
ISSUE CLASSIFICATION		

IN THE CIRCUIT COURT OF THE FIRST JUDICIAL CIRCUIT IN AND FOR THE COUNTY OF DADE, FLORIDA

OLP!

PATENT DATE

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1997

APPLICATION NO. 09/902051	CONT/PRIOR D	CLASS 369	SUBCLASS 25	ART UNIT 2651	EXAMINER Davidson
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PLICANTS Mark Chambers
Scott Kaylor
Jose Perez
Alan Chaiken

360

CMOS DC offset correction circuit with programmable high-pass transfer function

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. _____	Figs. Drwg. _____	Print Fig. _____	Total Claims _____	Print Claim for O.G. _____
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) _____ (Date)			ISSUE FEE	
	_____ (Legal Instruments Examiner) _____ (Date)			Amount Due _____	Date Paid _____
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Date)			ISSUE BATCH NUMBER	

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